

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kimoto

Serial No.: 09/850,254

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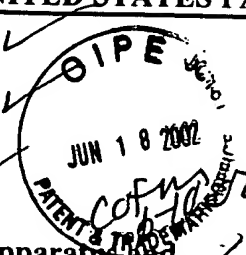
Title: Semiconductor Apparatus and
Manufacturing Method Therefor

Attorney Docket No.: N13476100S

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Examiner: Quinto, K.



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RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The following is submitted in response to the Office Action dated March 13, 2002, and is currently due June 13, 2002.

Entry of the following amendments is respectfully requested.

37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: June 10, 2002

Typed Name: Darryl Walker

Signature: Darryl Walker